

## ABSTRACT OF THE DISCLOSURE

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An enhanced CT detector module design utilizing a simplified FET mode option that effectively sums selected detector cells in X, allowing a coupling of scan slices in Z with the same or less number of DAS channels. In an embodiment of this invention wherein some cells float (i.e. they are left open) their collected charge will automatically re-distribute itself among the neighboring cells. This embodiment will allow cell summing in the x direction with a much simpler interconnect scheme i.e. far fewer FET switches and simplified decoder. Ideally there can be no increased in the number of FET switches/detector pixel)